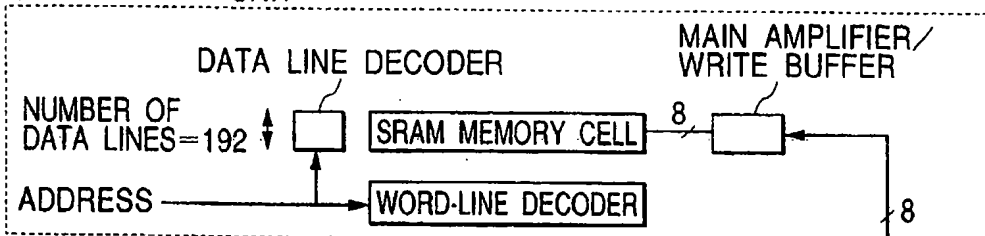


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FIG. 5**SRAM MACRO UNIT****DRAM MACRO UNIT**